for an extension of time. In response to the office action, please consider the following remarks.

REMARKS

Claims 1-22 are pending. Claims 1-4 and 16-17 stand rejected under 35 U.S.C. § 102(e). Claims 5-15 and 19-22 stand rejected under 35 U.S.C. § 103(a). Claims 9-11 stand objected to as being dependent upon a rejected base claim.

The Examiner has rejected claims 1-4 and 16-17 under 35 USC § 102(e) as anticipated by Chuang et al. (US Patent Application Publication No. US 2003 / 0078763 A1).

The Examiner has rejected claims 5-15 and 19-22 under 35 U.S.C. § 103(a) as obvious over Chuang in view of Shepard, K.L. et al. "Body-Voltage Estimation in Digital PD-SOI Circuits and its Application to Static Timing Analysis". 1999 IEEE/ACM Int'l Conf. on CAD. Nov. 7-11, 1999. pp. 531-538.

Applicant submits herewith a Declaration Under 37 C.F.R. §1.131 signed by the inventor of the present invention, Kenneth L. Shepard, setting forth facts showing the conception and reduction to practice of the invention of claims 1-22 prior to the April 19, 1999 filing date of Chuang.

In view of applicant's prior invention of the subject matter of claims 1-22, Chuang is no longer an effective reference under 35 USC § 102(e). Additionally, Chuang is no longer an effective reference under 35 U.S.C. § 103(a). Therefore, Applicant respectfully requests that the rejections under 35 USC § 102(e) and 35 U.S.C. § 103(a) be withdrawn.

Respectfully submitted,

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